**Lab Class- 5**

**02-10-23**

**Ex-5:** Design and generate a 1 to 4 demultiplexer.

**Tutorial-**

**1. Demultiplexer Using Logic Gates-**

<https://youtu.be/yRoCCjPzmc4?si=n5iWbjLYh0flbGFx>

**2. Demultiplexer Using 74155 IC-**

<https://youtu.be/BiENX_jjSGE?si=ACRsIAeDeu6V3eiw>